

EAST - (09081472.wsp:1)

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☐ Failed  
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L21: (8913) output near4 ((address w  
 L22: (7811) output near3 ((address w  
 L23: (380463) input with tim\$3  
 L24: (477) L23 same L22  
 L25: (6) L24 and ((ISO isolation) wi  
 L39: (180) ((low lo L) near2 isolati

(395) (timing adj3 circuit) and input  
 (340) ((timing adj3 circuit) and inp  
 (225) (((timing adj3 circuit) and in  
 (5) (((timing adj3 circuit) and in  
 (166) (((timing adj3 circuit) and in

DBs: USPAT,US,PCPUB,EPO,IPD,DERWENT,IBM,TDB  
 Default operator: DA  
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☒ Highlight all hit terms initially

L24 and ((ISO isolation) with (sense adj2 (amp amplifier)))

☒ BRS form  
☒ ISR form  
☐ Image  
☐ Text  
☐ HTML

	Current OR	Current XRef	Retrieval C	Inventor	S	C	P	2	3	4	5	Image Doc. Dis	PT
1	365/233			Sawhney, Ramandeep S.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 20020191478	<input type="checkbox"/>
2	365/200	365/227		Hidaka, Hideto	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 20020008999	<input type="checkbox"/>
3	365/189.01	365/189.05		Butler, Edward et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 5392241	<input checked="" type="checkbox"/>
4	365/208	365/189.01; 365/206;		McLaury, Loren L.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 5323350	<input type="checkbox"/>
5	365/230.06	365/189.01; 365/203;		Zagar, Paul S. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 5311478	<input type="checkbox"/>
6	365/189.01	365/203; 365/230.06		McLaury, Loren L.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 5265050	<input type="checkbox"/>

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NUM

L Number	Hits	Search Text	DB	Time stamp
21	8913	output near4 ((address word-line wordline row) near2 decod\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/01/19 12:36
22	7811	output near3 ((address word-line wordline row) near2 decod\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/01/19 12:36
23	380463	input with tim\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/01/19 12:37
24	477	(input with tim\$3) same (output near3 ((address word-line wordline row) near2 decod\$3))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/01/19 12:37
25	6	((input with tim\$3) same (output near3 ((address word-line wordline row) near2 decod\$3))) and ((ISO isolation) with (sense adj2 (amp amplifier)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/01/19 12:49
39	180	((low lo L) near2 isolation near3 signal)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/01/19 14:23
-	395	(timing adj3 circuit) and input and output and (isolat\$3 with signal) and (address adj3 decoder)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/08/04 19:23
-	340	((timing adj3 circuit) and input and output and (isolat\$3 with signal) and (address adj3 decoder)) and (input adj3 signal)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/08/04 19:23
-	225	((((timing adj3 circuit) and input and output and (isolat\$3 with signal) and (address adj3 decoder)) and (input adj3 signal)) and (RAS (row adj2 address)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/08/04 19:27
-	5	(((((timing adj3 circuit) and input and output and (isolat\$3 with signal) and (address adj3 decoder)) and (input adj3 signal)) and (RAS (row adj2 address))) and (low adj2 isolation))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/08/04 19:27
-	166	((((timing adj3 circuit) and input and output and (isolat\$3 with signal) and (address adj3 decoder)) and (input adj3 signal)) and (RAS (row adj2 address adj2 signal)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/08/04 19:29
-	3	(((((timing adj3 circuit) and input and output and (isolat\$3 with signal) and (address adj3 decoder)) and (input adj3 signal)) and (RAS (row adj2 address adj2 signal))) and (low adj2 isolation))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/08/04 19:29
-	138	((((timing adj3 circuit) and input and output and (isolat\$3 with signal) and (address adj3 decoder)) and (input adj3 signal)) and (RAS (row adj2 address adj2 strobe)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/08/04 19:35

-	3	((((timing adj3 circuit) and input and output and (isolat\$3 with signal) and (address adj3 decoder)) and (input adj3 signal)) and (RAS (row adj2 address adj2 strobe))) and (low adj2 isolation)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/08/05 18:48
-	0	(input with (isolation adj2 signal)) and (output with (address adj2 decoder)) and ((timing adj2 circuit) same (input adj2 signal))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/08/04 19:32
-	44	((((timing adj3 circuit) and input and output and (isolat\$3 with signal) and (address adj3 decoder)) and (input adj3 signal)) and (RAS (row adj2 address adj2 strobe)) and (and\$1 adj2 gate) and (or\$1 adj2 gate)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/08/05 17:30
-	1017	((digit or bit) adj2 line) and (sense adj2 (amp or amplifier)) and (isolat\$3 adj3 (gate or signal))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/08/04 20:20
-	6	("5970022" "4571705" "5132931" "5335206" "5410508" "5459846").pn.	USPAT	2002/08/06 12:05
-	6	((("5970022" "4571705" "5132931" "5335206" "5410508" "5459846").pn.) and (input isolat\$3 signal output address decod\$3 timing circuit activat\$3 initiat\$3 trigger\$3 RAS NOR\$3 AND\$3 wordline)	USPAT	2002/08/06 17:47
-	6	(timing adj2 circuit) same (input adj2 signal) same (isolat\$3 adj2 signal)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/08/06 19:30
-	2	((timing adj2 circuit) same (address adj2 decoder)) and (input adj2 signal) and (isolat\$3 adj2 signal) and output	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/08/06 19:33
-	3	((timing adj2 circuit) same (address adj2 decoder)) and input and (isolat\$3 adj2 signal) and output	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/08/06 19:34
-	976	((sense adj2 (amp amplifier)) with (shar\$3 switch\$3 select\$3)) same (second with array)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/08/07 17:04
-	141	((sense adj2 (amp amplifier)) with (shar\$3 switch\$3 select\$3)) same (second with array)) and (timing adj3 circuit)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/08/07 17:17
-	95	((sense adj2 (amp amplifier)) with (shar\$3 switch\$3 select\$3)) same (second with array)) and (timing adj3 circuit)) and ((address wordline) adj3 decoder)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/08/07 17:31
-	24	((sense adj2 (amp amplifier)) with (shar\$3 switch\$3 select\$3)) same (second with array)) and (timing adj3 circuit)) and ((address wordline) adj3 decoder)) and (isolation or ISO)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/08/09 12:09
-	28	((sense adj2 (amp amplifier)) with (shar\$3 switch\$3 select\$3)) same (second with (array bank block))) and (timing adj3 circuit)) and ((address wordline) adj3 decoder)) and (isolation or ISO)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/01/17 13:38
-	1	5970022.pn.	USPAT	2003/01/17 13:15

-	272	{sense adj2 (amp amplifier)) with (ISO (isolation adj2 signal))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/01/17 13:40
-	11	((sense adj2 (amp amplifier)) with (ISO (isolation adj2 signal))) same (input and output)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/01/17 13:40
-	9	((sense adj2 (amp amplifier)) with (ISO (isolation adj2 signal))) same (input and output)) and (decod\$3 near4 (address wordline))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/01/19 12:48